


09/28/00

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

 O.I.P.E. SCANNED <u>TM<sup>3</sup></u> Q.A. <u>CTH</u>	PATENT DATE
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APPLICATION NO: 09/671065	CONT/PRIOR	CLASS 710	SUBCLASS 129	ART UNIT 285 2101	EXAMINER VO
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## APPLICANTS

TITLE	APPLICANTS
1. <b>Chairman</b>	1. <b>Mr. J. H. ...</b>
2. <b>Vice Chairman</b>	2. <b>Mr. J. H. ...</b>
3. <b>Secretary</b>	3. <b>Mr. J. H. ...</b>
4. <b>Treasurer</b>	4. <b>Mr. J. H. ...</b>
5. <b>Member</b>	5. <b>Mr. J. H. ...</b>
6. <b>Member</b>	6. <b>Mr. J. H. ...</b>
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93. <b>Member</b>	93. <b>Mr. J. H. ...</b>
94. <b>Member</b>	94. <b>Mr. J. H. ...</b>
95. <b>Member</b>	95. <b>Mr. J.</b>

Mechanism for allowing PCI-PCI bridges to cache data without any  
coherency side effects

PTO-2040

[illegible]

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<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
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